

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. . . . . 09/512,149  
Filing Date . . . . . February 23, 2000  
Inventor . . . . . Vishnu K. Agarwal  
Assignee . . . . . Micron Technology, Inc.  
Group Art Unit . . . . . 2814  
Examiner . . . . . M. Pizarro-Crespo  
Attorney's Docket No. . . . . MI22-1322  
Title: Integrated Circuitry Including a Capacitor With an Amorphous and a  
Crystalline High K Capacitor Dielectric Region (as Amended)



#9/Amend A  
Adams  
3/9/01

**RESPONSE TO NOVEMBER 9, 2000 OFFICE ACTION**

To: Assistant Commissioner for Patents  
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)  
Wells, St. John, Roberts, Gregory & Matkin P.S.  
601 West First Avenue, Suite 1300  
Spokane, WA 99201-3828

Responsive to the Office Action dated November 9, 2000, Applicant  
amends and remarks as follows:

**AMENDMENTS**

**In the Title**

Please delete the existing title and substitute the following in its place:

--INTEGRATED CIRCUITRY INCLUDING A CAPACITOR WITH AN  
AMORPHOUS AND A CRYSTALLINE HIGH K CAPACITOR DIELECTRIC  
REGION--.

RECEIVED  
MAR - 8 2001  
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